

CXL: Advancing coherent connectivity

August 2023



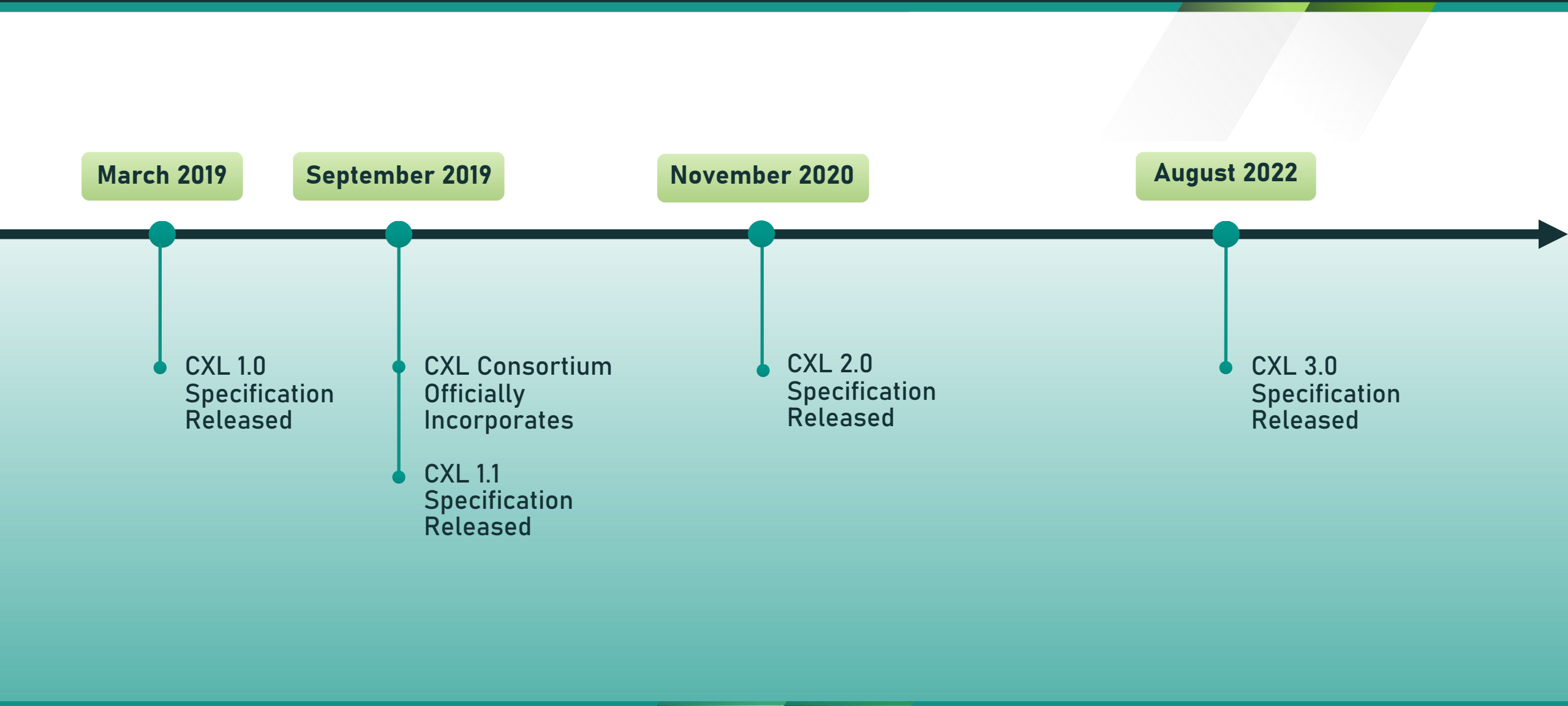
CXL Board of Directors



Industry Open Standard for High Speed Communications

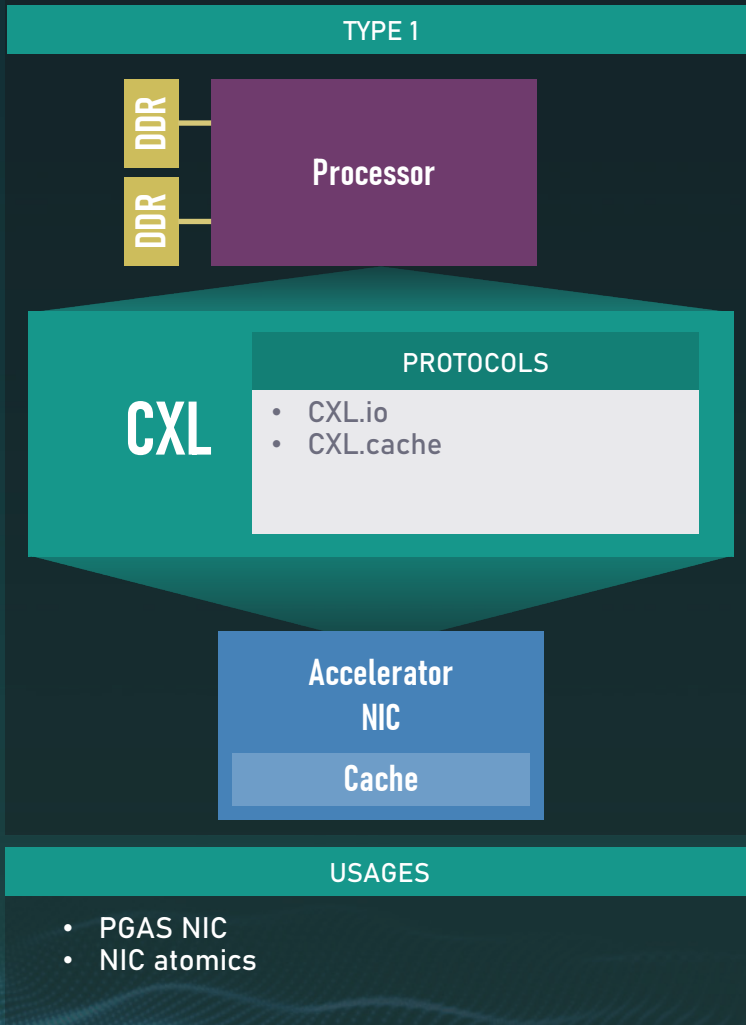
250+ Member Companies

CXL Specification Release Timeline

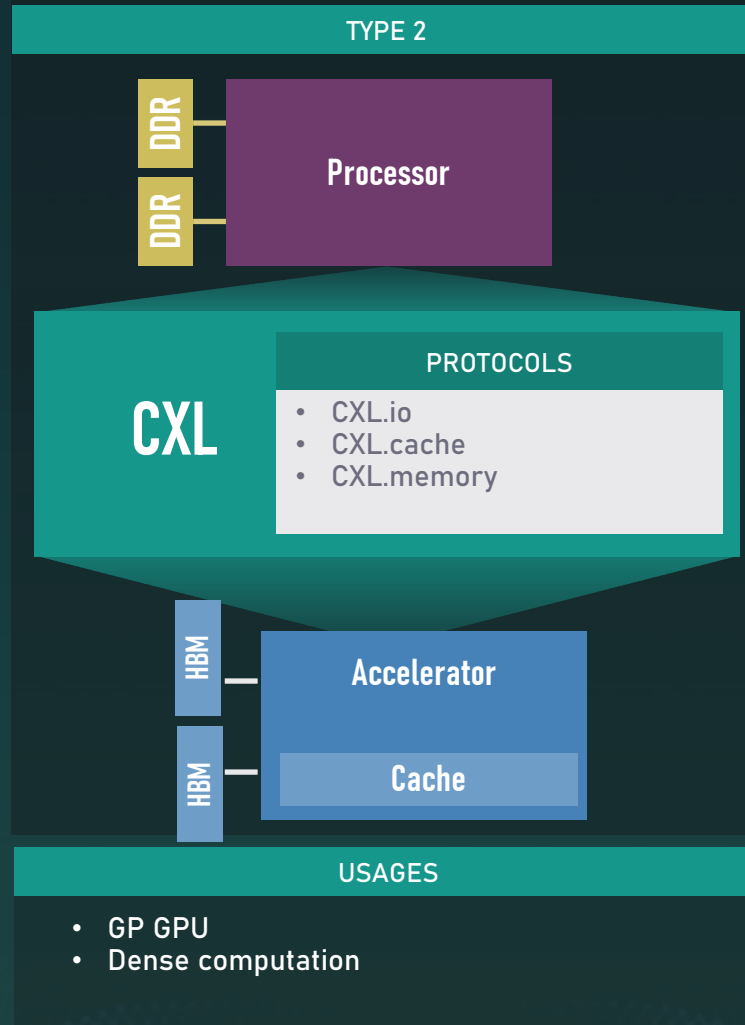


Representative CXL Usages

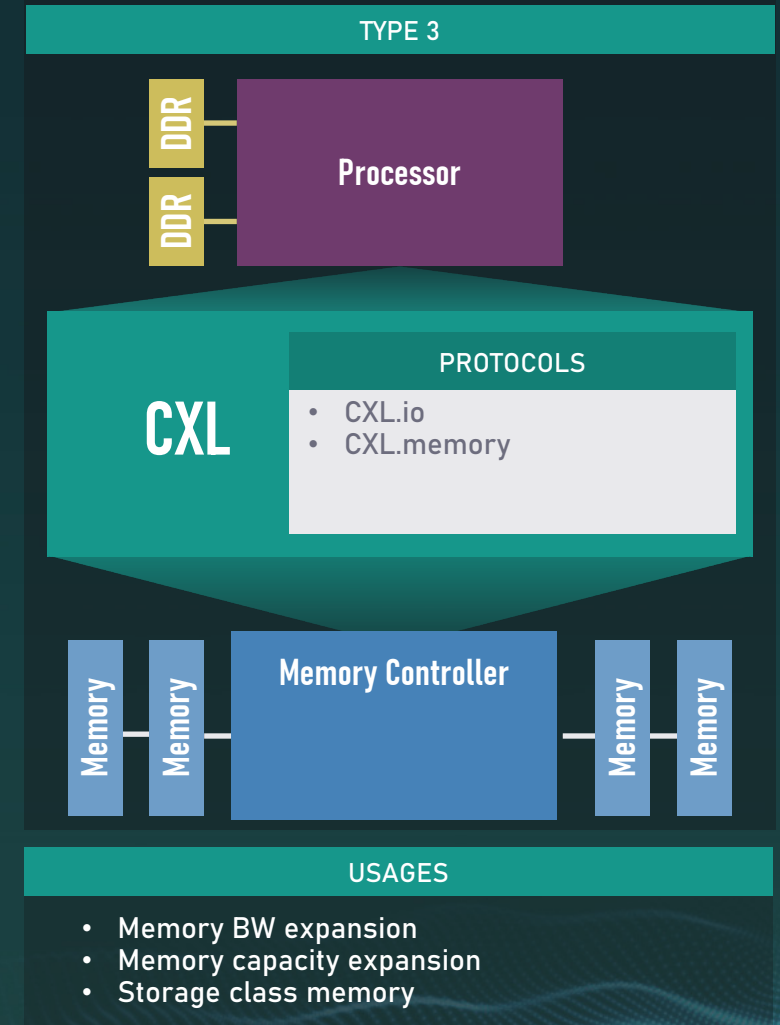
Caching Devices / Accelerators



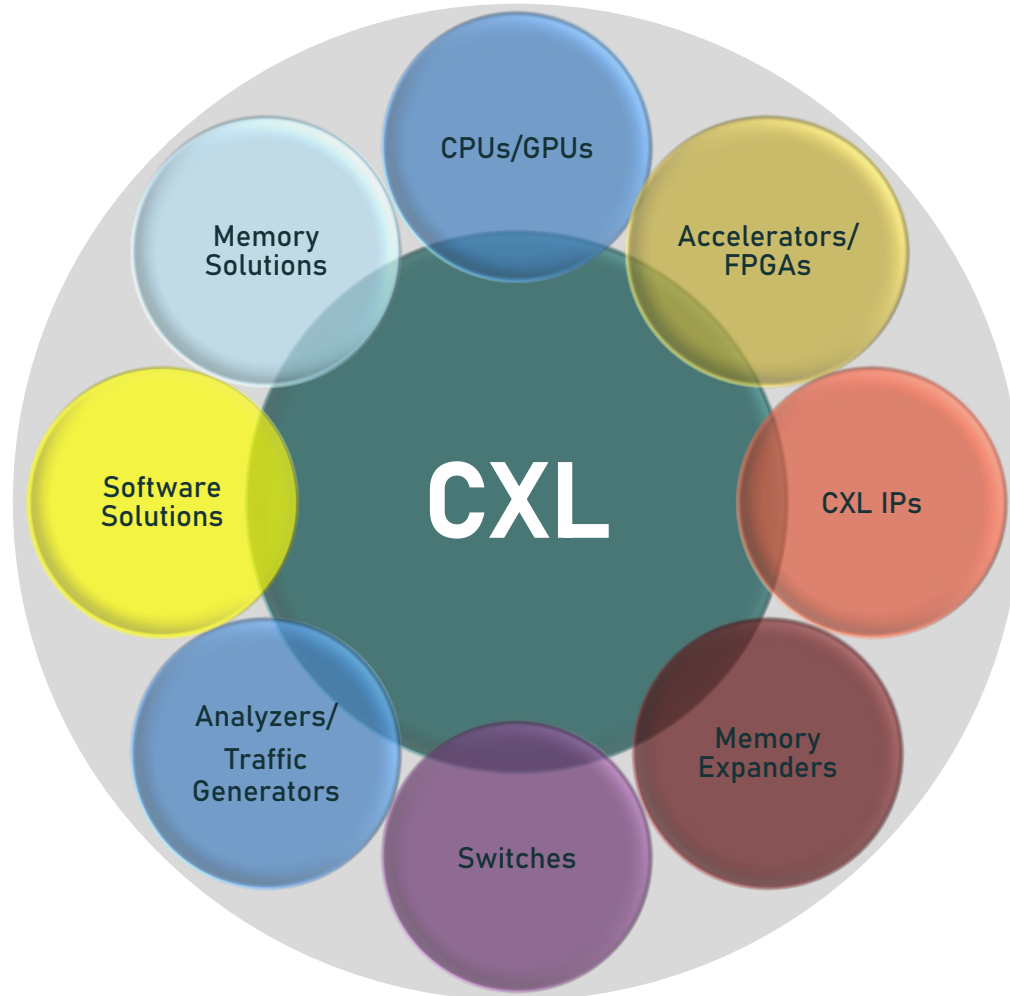
Accelerators with Memory



Memory Buffers

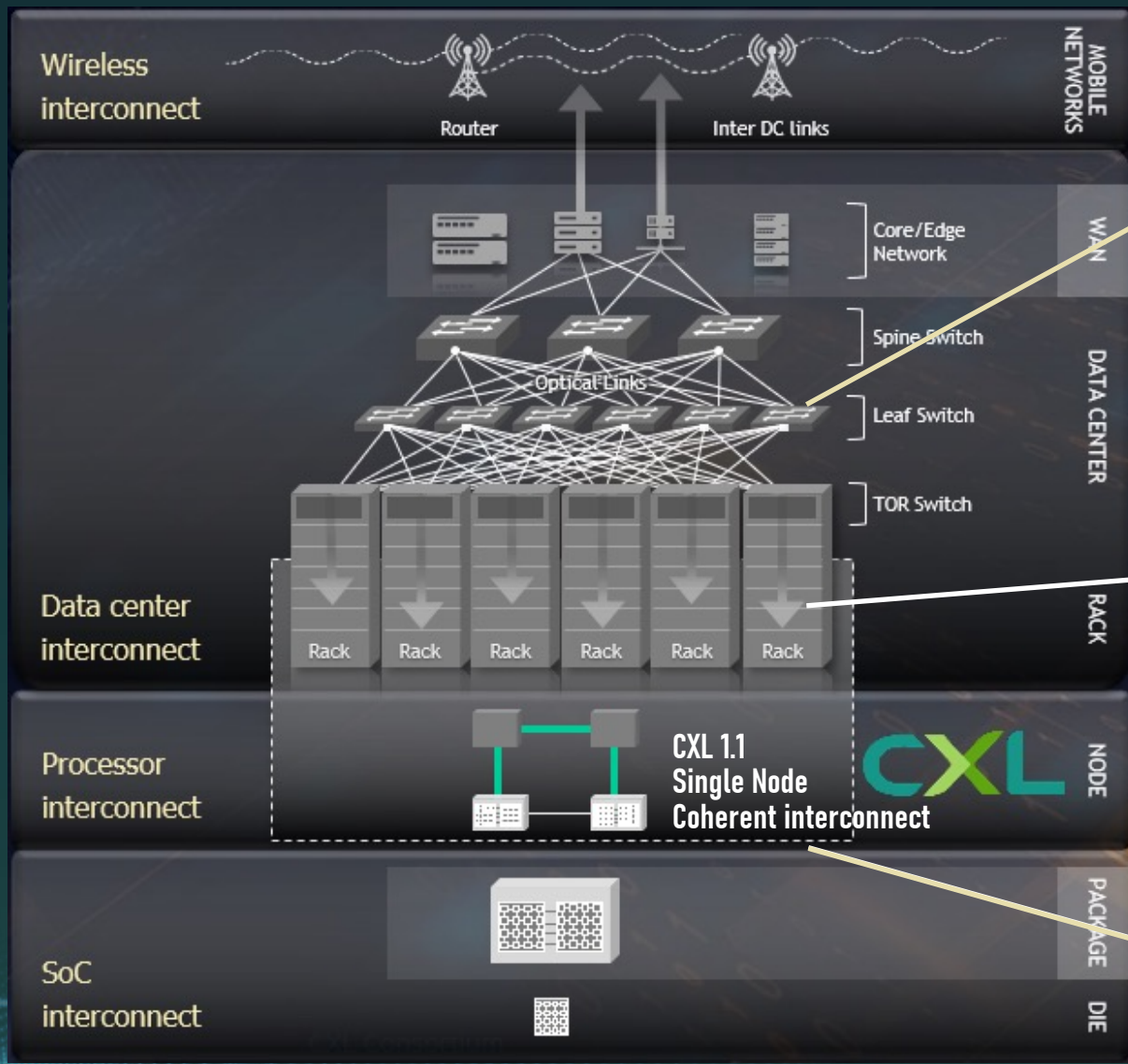


Growth of CXL Ecosystem since it's inception

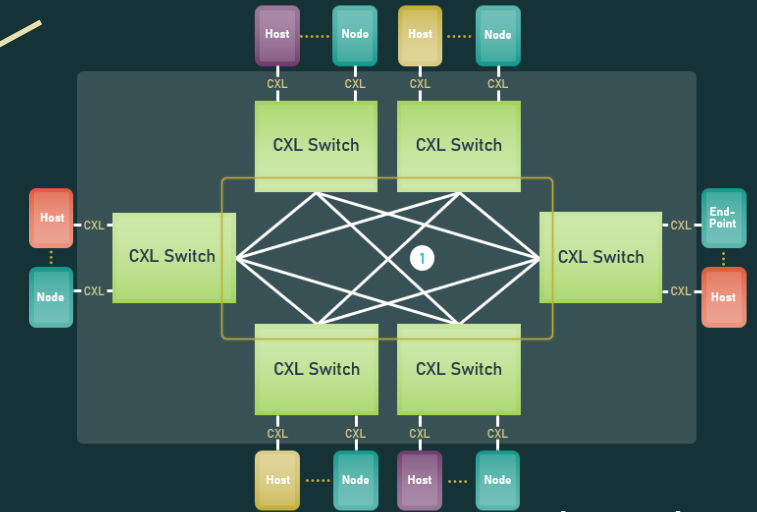


- Ecosystem that meets the ever-increasing performance and scale requirements
- Fully backwards compatible
- Lowers overall system cost
- Comprehensive compliance and testing support

Data Center: Expanding Scope of CXL

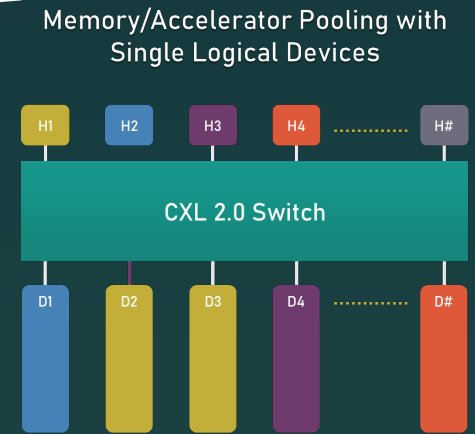


CXL 3.0

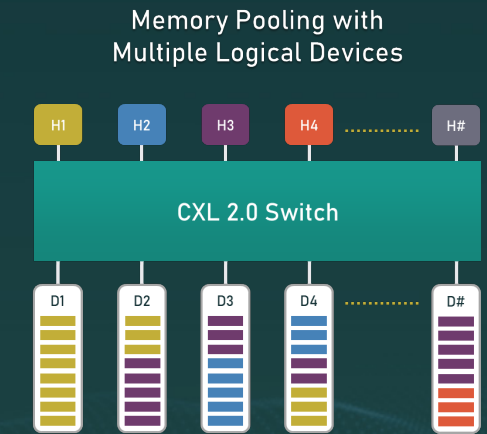


Composable Fabric growth for disaggregation/pooling/accelerator

CXL 2.0

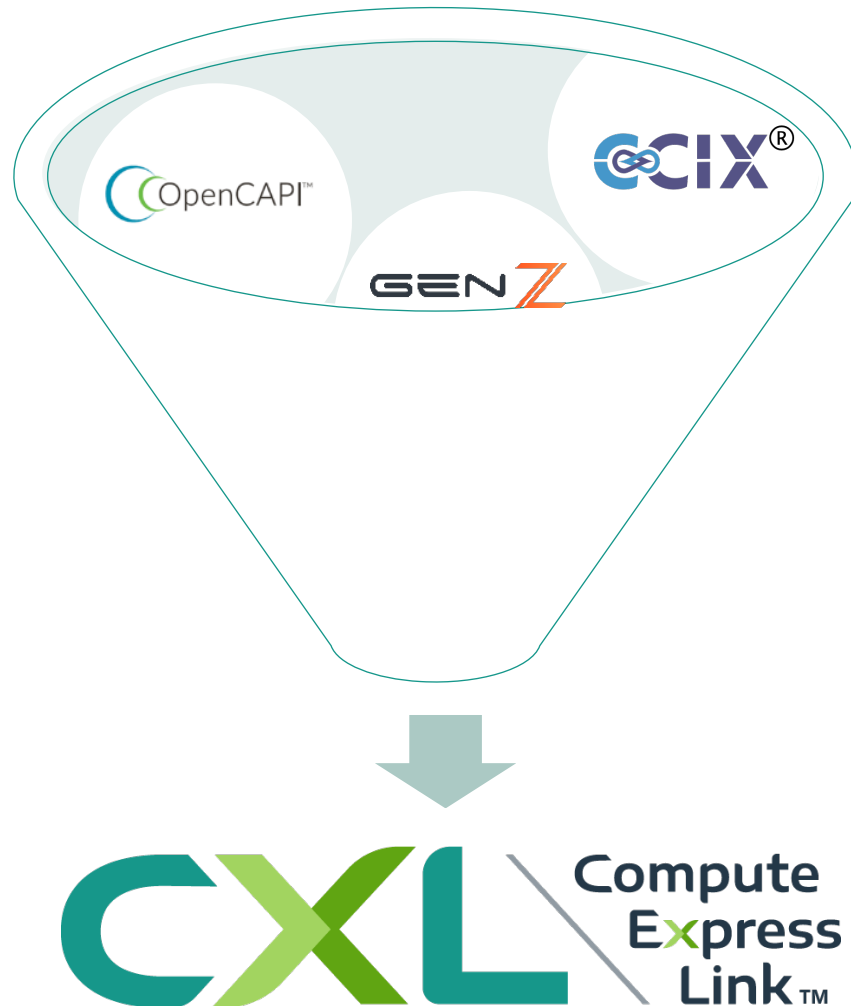


Memory/Accelerator Pooling with Single Logical Devices



Memory Pooling with Multiple Logical Devices

Multiple Nodes inside a Rack/ Chassis supporting pooling of resources



CXL is emerging as the industry focal point for coherent IO



August 3, 2023, CXL Consortium and CCIX Consortium Sign Letter of Intent to Transfer CCIX Assets to CXL



August 1, 2022, CXL Consortium and OpenCAPI Consortium Sign Letter of Intent to Transfer OpenCAPI Assets to CXL



February 2022, CXL Consortium and Gen-Z Consortium signed agreement to transfer Gen-Z specification and assets to CXL Consortium

Call to Action

- Download the CXL 3.0 specification via www.ComputeExpressLink.org
- Support future specification development by joining the Consortium
- Follow [@CXL Consortium](https://twitter.com/CXLConsortium) on social media for updates!



Thank You

www.ComputeExpressLink.org