CXL: The Dawn of Big Memory

Charles Fan Co-founder & CEO MemVerge



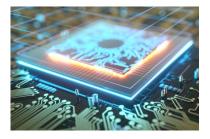
with the wit

The Rise of Modern Data-Centric Applications

AI/ML



EDA Simulation



Video Rendering



Financial Analytics



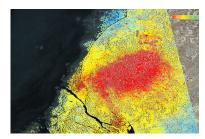
Genomics

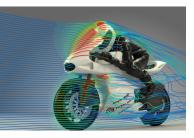
Geophysical



Risk Analysis











Opening the Door to the Era of Big Memory



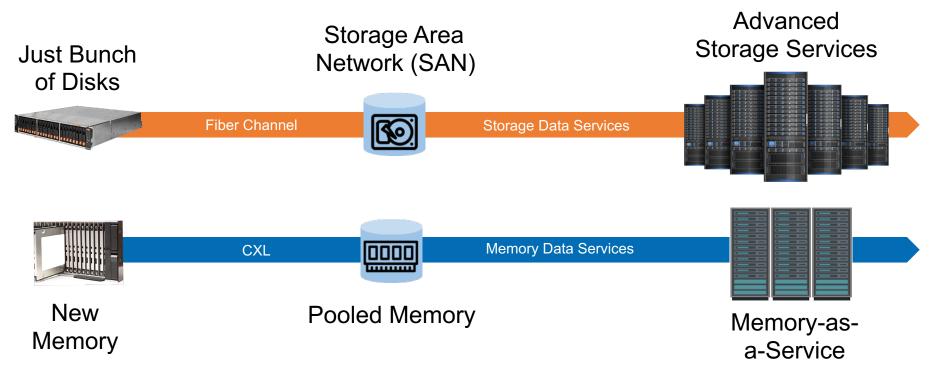
Abundant Composable Available



What happened 30+ years ago

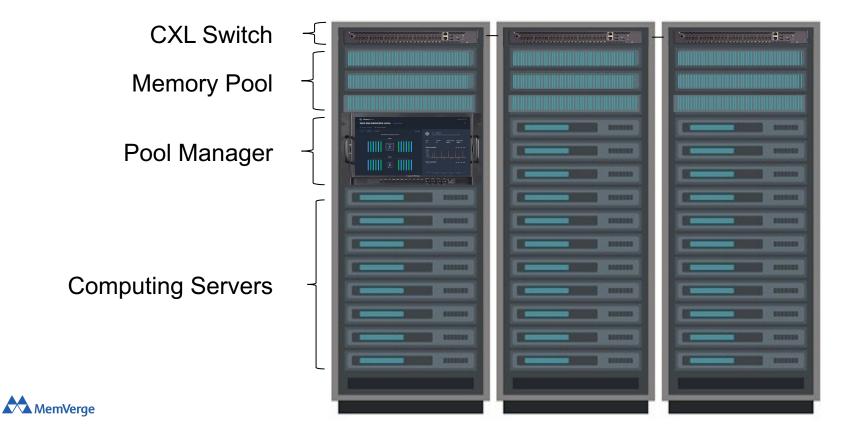


Where We Are Going...



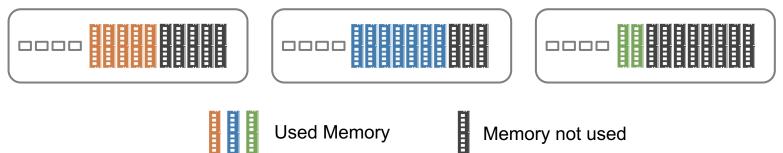








Before CXL



Azure Paper*:

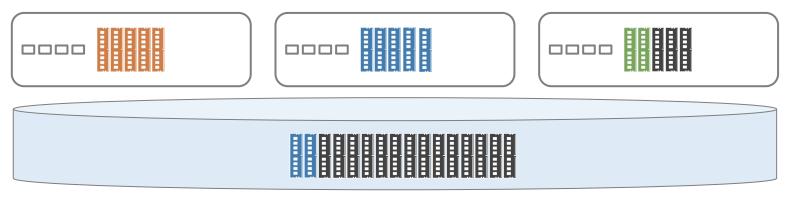
- Up to 50% of server costs is from DRAM alone
- Up to 25% of memory is stranded
- 50% of all VMs never touch 50% of their rented memory

* H. Li et. Al. First-generation Memory Disaggregation for Cloud Platforms. arXiv:2203.00241v2 [cs.OS], March 5, 2022





After CXL



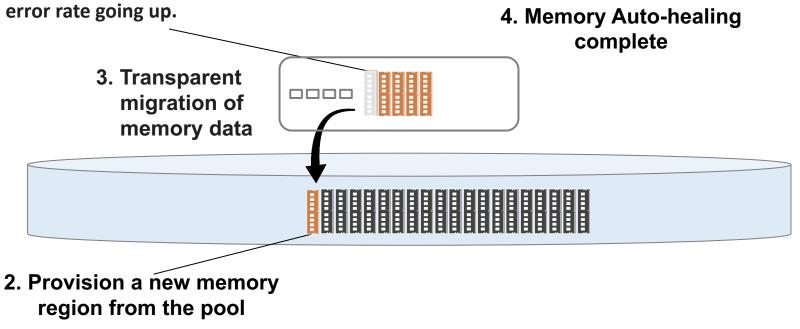


Memory disaggregation can save billions of dollars per year.





1. A memory module is becoming bad:

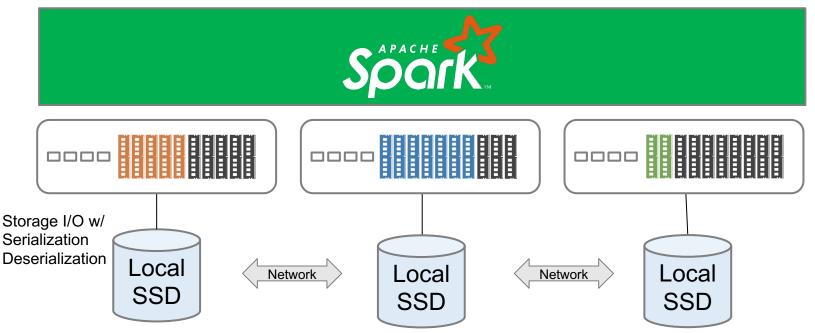






Distributed Data Shuffling

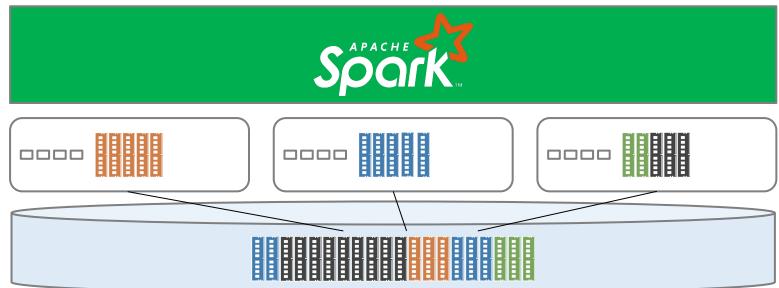
Before CXL







After CXL

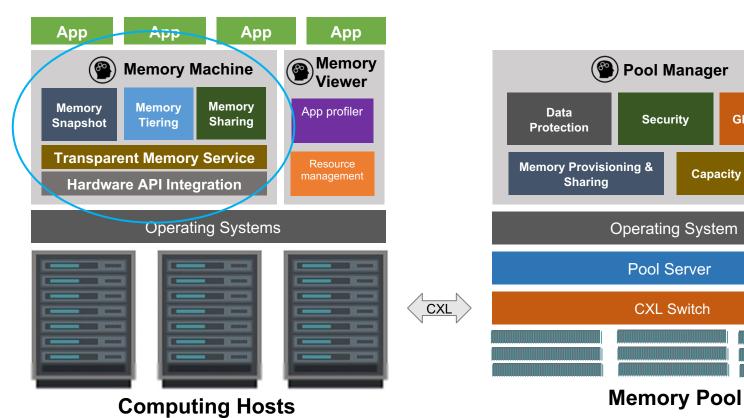


Project Splash is open source: https://github.com/MemVerge/splash

S. Chen, et. Al. Optimizing Performance and Computing Resource Management of in-memory Big Data Analytics with Disaggregated Persistent Memory. CCGRID'19

MemVerge

Link Key Software Components

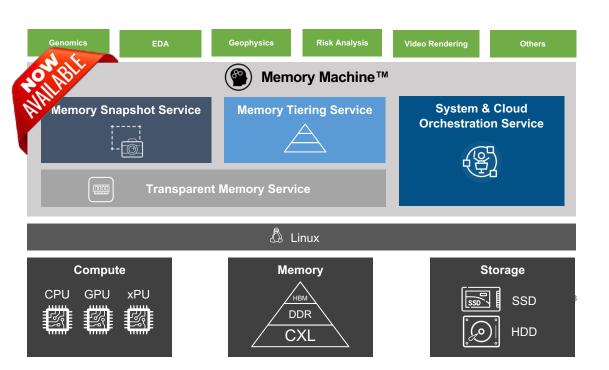




Global Insights

Capacity Optimization

Announcing Memory Machine Cloud Edition



MemVerae

Memory Capacity Expansion

- Software-defined Memory Pool with intelligent Auto-tiering
- No application change required

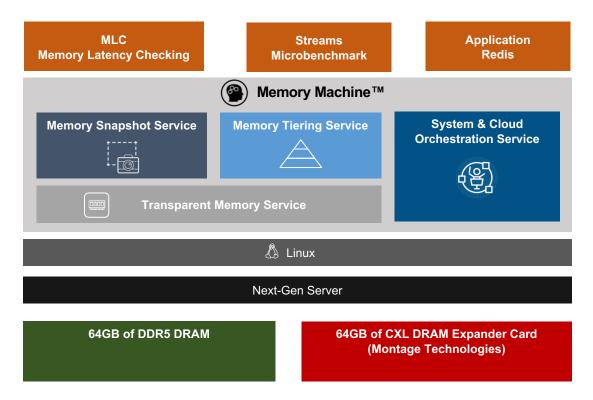
Accelerate Time-to-discovery

- Transparent checkpointing
- Roll-back, restore and clone anywhere any time

Reduce Cloud Cost by up to 70%

- Enable long-running applications to use low-cost Spot instances
- Integration with cloud automation and scheduler to auto-recover from CSP preemptions

Early Results Running Memory Machine on CXL

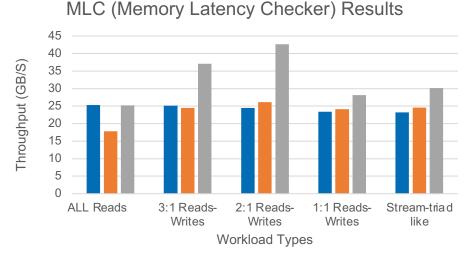




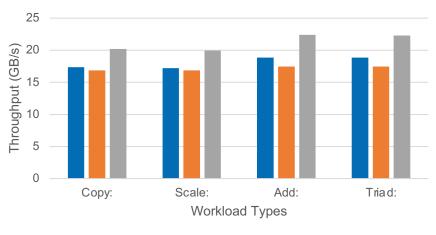
Early Results Running Memory Machine on CXL

DDR5 Only

CXL Only





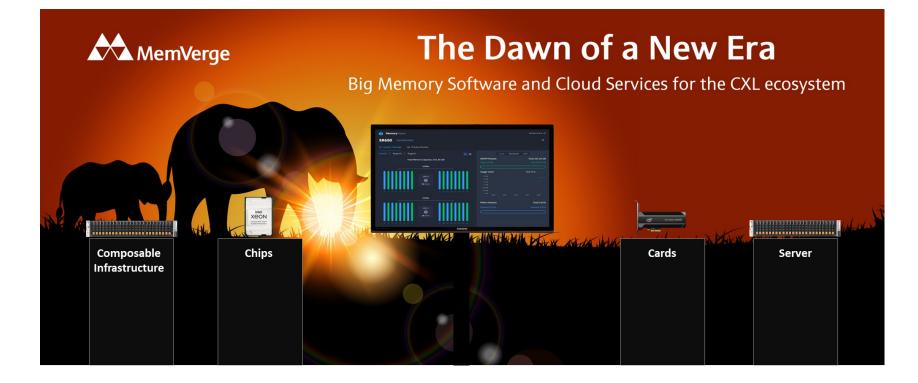


■ DDR+CXL Memory Machine Auto-Tiering

Stream Results

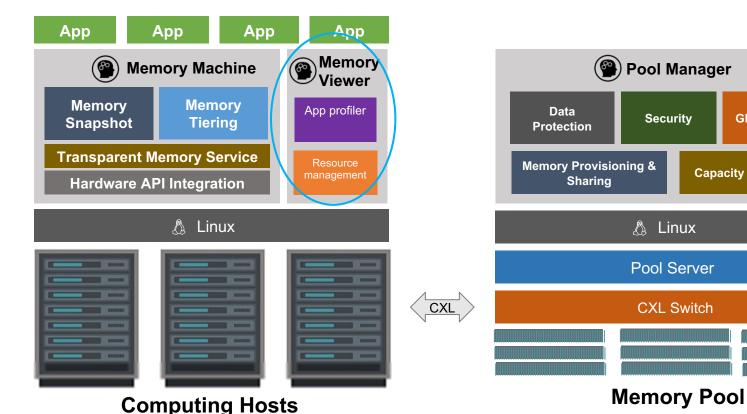
MemVerge

Live Demos at MemVerge Booth





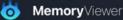
Key Software Components





Global Insights

Capacity Optimization



System Topology

Region0

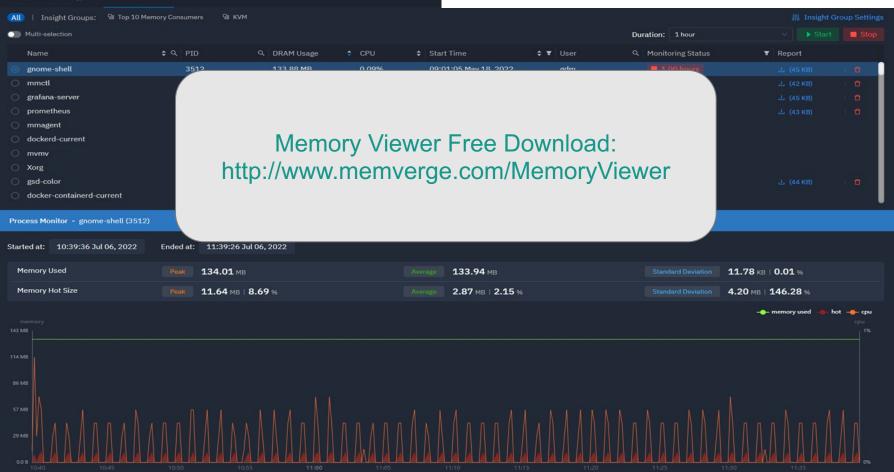
Announcing 👌 Memory Viewer SE02.ENG.MEMVERGE.LOCAL AVAILABLE Process Monitor Region1 **E** ш CPU - Socket 0 Total Memory Capacity: 1.66 TB Model Intel(R) Xeon(R) Gold 6240 CPU @ 2.60GHz DIMMs Cores Threads Current Speed Max Speed 18 36 2600 MHz 4000 MHz CPU 0 Ö **DRAM Bandwidth** ---- read --- write 18 Cores DIMMs **PMem Bandwidth** ---- read ---- write CPU1 0 18 Cores 0.0 B/s 18

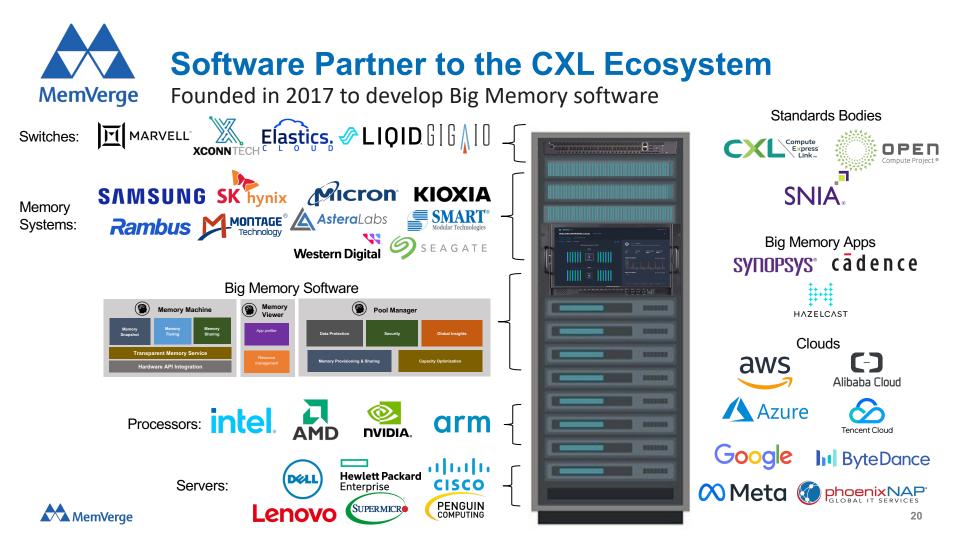
Version v0.7.1 (i)

System Topology

SE02.ENG.MEMVERGE.LOCAL Process Monitor

Application Memory Heatmap







Memorize the future.

Please visit our booth for the live demos